PIB-C

Counter board.

The counter board has 8 inputs. The inputs are isolated from the rest of the system with opto-couplers. The inputs are suited for a signal from 5 through 24 V. The board counts the number of pulses on the input. The inputs are filtered at 100 Hz. The maximum input frequency is 50 Hz. The minimum pulse width is 10ms.

There are jumpers to connect the '-' input to the system ground. The '+' input can be connected to the system +15V with a 2k7 pull-up resistor for open collector signals.

The technical specifications are:

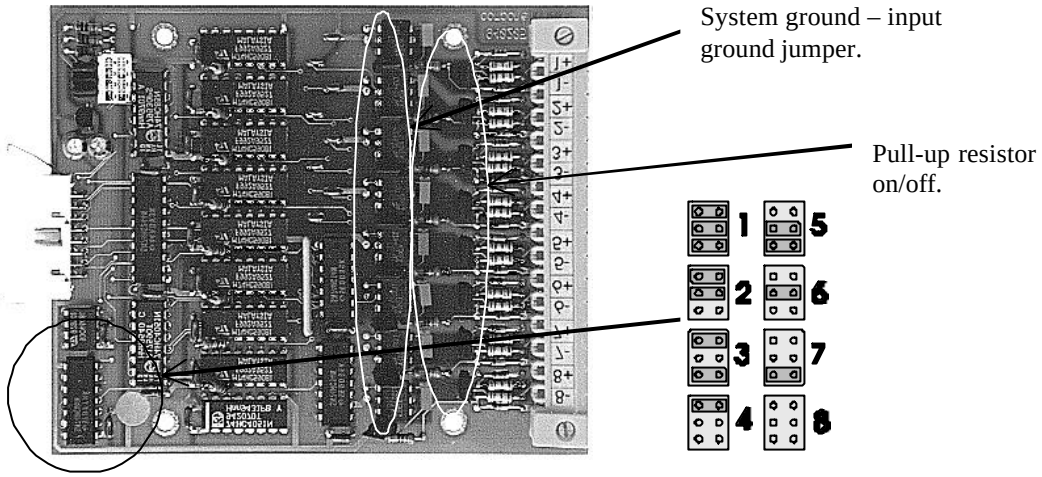
Width in TE's (see PRW)	4
Power requirements:	+15VDC, 40 mA
Measuring channels: #1 through 8	pulse counter inputs
Minimum input voltage:	5 V
Maximum input voltage:	24 V
Pull up resistor to +15 VDC:	2k7
Input impedance:	approx. 2k7
Jumpers:	board address
	Input ground – system ground
	Pull-up resistor to 15 VDC
Maximum input frequency:	50 Hz
Minimum pulse width:	10 ms
Isolation:	500 V max.
Operating temperature:	-10...+50°C
Storage temperature:	-20...+70°C

The signal connections are:

Connector pin number	Signal
1	- signal 8
2	+ signal 8
3	- signal 7
4	+ signal 7
5	- signal 6
6	+ signal 6
7	- signal 5
8	+ signal 5
9	- signal 4
10	+ signal 4
11	- signal 3
12	+ signal 3
13	- signal 2

14	+ signal 2
15	- signal 1
16	+ signal 1

The jumpers are set as follows:



The pull-up resistor is connected to the system +15 V. If a pull-up resistor is used, the System ground - input ground jumper must also be installed.

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