

PIB-S

Digital input board.

The digital input board has 8 CMOS/TTL signal level inputs. The inputs have a common ground, which is connected to the system ground. This board is meant for low cost interfacing to systems that may be connected to the PRO-NET system ground.

A +5V supply is available for external circuitry. Total current drawn from this +5V supply should not exceed 50mA. All inputs have internal pull-up resistors.

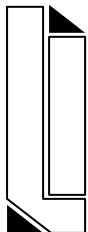
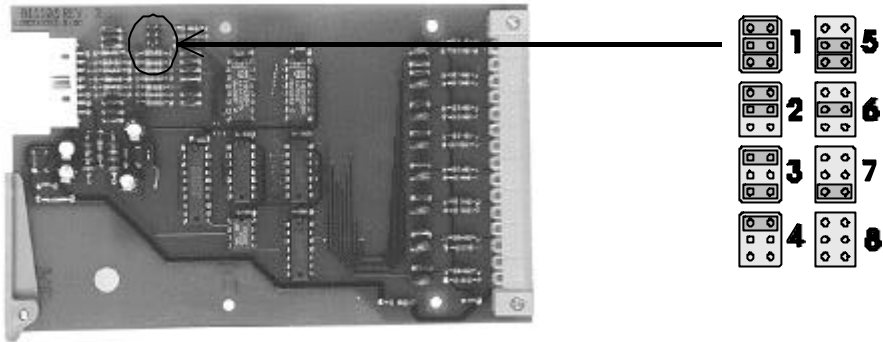
The technical specifications are:

Width in TE's (see PRW)	4
Power requirements:	+15VDC, 7 mA
Measuring channels: #1 through 8	0...5 VDC CMOS/TTL level inputs
Jumpers:	board address
Operating temperature:	-10...+50°C
Storage temperature:	-20...+70°C

The signal connections are:

Connector pin number	Signal
1	Channel 8
2	0 V
3	Channel 7
4	+ 5 VDC
5	Channel 6
6	0 V
7	Channel 5
8	+ 5 VDC
9	Channel 4
10	0 V
11	Channel 3
12	+ 5 VDC
13	Channel 2
14	0 V
15	Channel 1
16	+ 5 VDC

The jumpers are set as follows:



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